

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	5	L1 and 718/102.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/27 12:32
L5	0	L1 and 718/107.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/27 12:32
L6	0	L1 and 718/108.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/27 12:32
L7	2	L1 and 718/104.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/27 12:32
L1	108	((detect\$3 determin\$5 estimat\$3 predict\$3 compute calculat\$3) with (thread\$3 task\$3 process job) with (execut\$3 run\$4) with (time cost) with (exceed\$3 overrun\$3) with (limit\$5 threshold bound\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/27 12:30
L2	7	L1 and "718"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/27 12:30
L3	0	L1 and 718/100.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/27 12:30
S77	107	((detect\$3 determin\$5 estimat\$3 predict\$3 compute calculat\$3) with (thread\$3 task\$3 process job) with (execut\$3 run\$4) with (time cost) with (exceed\$3 overrun\$3) with (limit\$5 threshold bound\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/27 12:30

S79	2	S78 and (slack\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 15:18
S80	1	S78 and (slack\$3 and deadline)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 15:18
S78	2	"20020120663".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 15:02
S76	281	((thread\$3 task\$3 process job) with (execut\$3 run\$4) with (time cost) with (exceed\$3 overrun\$3) with (limit\$5 threshold bound\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 13:17
S74	43	S72 and (((execut\$3 run\$4) near3 (time cost)) with (limit\$5 threshold bound\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 13:15
S75	31	S74 not S73	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 12:35
S73	13	S72 and (((addition\$2 extend\$3 extension slack\$3) near3 (time cost)) with (limit\$5 threshold bound\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 12:34
S72	159	S69 same (((addition\$2 extend\$3 extension slack\$3) near3 (time cost)) ((time cost) near4 (overrun\$3 exceed\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 12:27
S70	158	S69 same (((addition\$2 extend\$3 extension) near3 (time cost)) ((time cost) near4 (overrun\$3 exceed\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 12:26

S68	49	S67 and (((addition\$2 extend\$3 extension) near3 (time cost)) ((time cost) near4 (overrun\$3 exceed\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 11:20
S71	153	S70 not S68	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 11:20
S67	105	(recalculat\$3 recomput\$3 revis\$3) with (job task\$3 process) with (execut\$3 run\$4) with (cost\$3 time)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 11:19
S69	3109	(calculat\$3 compute estimat\$3 revis\$3) with (job task\$3 process) with (execut\$3 run\$4) with (cost\$3 time)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 11:19
S65	47	(updat\$3 revis\$3) with (schedul\$3) with (perform\$3 execut\$3) with (job task\$3) with (cost\$3 time)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/25 10:27

Day : Friday
Date: 5/27/2005

Time: 12:02:28

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = BOLLELLA

First Name = GREGORY

Application#	Patent#	Status	Date Filed	Title	Inventor Name 10
<u>60348105</u>	Not Issued	159	10/23/2001	SYSTEM AND METHOD FOR ASYNCHRONOUS TRANSFER OF CONTROL	BOLLELLA, GREGORY
<u>60343793</u>	Not Issued	159	10/23/2001	METHOD AND APPARATUS FOR SCOPED MEMORY	BOLLELLA, GREGORY
<u>10279348</u>	Not Issued	030	10/23/2002	METHOD AND APPARATUS FOR SCOPED MEMORY	BOLLELLA, GREGORY
<u>10279168</u>	Not Issued	030	10/23/2002	SYSTEM AND METHOD FOR ASYNCHRONOUS TRANSFER OF CONTROL	BOLLELLA, GREGORY
<u>09782780</u>	Not Issued	071	02/13/2001	SCHEDULING OPTIMIZATION HEURISTIC FOR EXECUTION TIME ACCUMULATING REAL-TIME SYSTEMS	BOLLELLA, GREGORY
<u>08975869</u>	Not Issued	161	11/21/1997	SYSTEM AND METHOD FOR SUPPORTING REAL-TIME COMPUTING WITHIN GENERAL PURPOSE SYSTEMS	BOLLELLA, GREGORY
<u>08975847</u>	<u>5974439</u>	150	11/21/1997	RESOURCE SHARING BETWEEN REAL-TIME AND GENERAL PURPOSE PROGRAMS	BOLLELLA, GREGORY
<u>08543252</u>	<u>5802053</u>	150	10/13/1995	TRANSPORT GATEWAY BETWEEN A NATIVE NETWORK AND A MIXED NETWORK	BOLLELLA, GREGORY
<u>08472365</u>	<u>6466962</u>	150	06/07/1995	SYSTEM AND METHOD FOR SUPPORTING REAL-TIME COMPUTING WITHIN GENERAL PURPOSE OPERATING SYSTEMS	BOLLELLA, GREGORY

Day : Friday
Date: 5/27/2005

 **PALM INTRANET**

Time: 12:03:08

Inventor Name Search Result

Your Search was:

Last Name = HAGGAR

First Name = PETER

Application#	Patent#	Status	Date Filed	Title	Inventor Name 5
10878297	Not Issued	030	06/28/2004	SYSTEM AND METHOD FOR USING SOAP TO INVOKE WEB SERVICES ON HANDHELD DEVICES	HAGGAR, PETER F.
10050272	6842759	150	01/16/2002	SINGLE-INSTANCE CLASS OBJECTS ACROSS MULTIPLE JVM PROCESSES IN A REAL-TIME SYSTEM	HAGGAR, PETER F.
09782780	Not Issued	071	02/13/2001	SCHEDULING OPTIMIZATION HEURISTIC FOR EXECUTION TIME ACCUMULATING REAL-TIME SYSTEMS	HAGGAR, PETER F.
09766518	6459392	150	01/19/2001	TECHNIQUE FOR ENCODING A SEQUENCE OF PERIODIC BYTE VALUES WITH VERTICAL CORRELATION	HAGGAR, PETER F.
09755241	6820183	150	01/05/2001	METHODS, SYSTEMS, AND COMPUTER PROGRAM PRODUCTS FOR MEMORY POOL MANAGEMENT USING VARIABLE SIZE SUB-POOLS	HAGGAR, PETER F.

Inventor Search Completed: No Records to Display.

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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = WENDT

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name 42
60549931	Not Issued	159	03/05/2004	REVERSE-FLOW PERFUSION OF THREE-DIMENSIONAL SCAFFOLDS	WENDT, DAVID
60543179	Not Issued	159	02/10/2004	BOTTOM BRACKET TORQUE SENSOR	WENDT, DAVID L.
60106861	Not Issued	159	11/03/1998	SHEARING ARRANGEMENT FOR SUBSEA UMBILICALS	WENDT, DAVID E.
29014313	D349920	150	10/15/1993	HANDHELD PRINTER FOR PRINTING ON SMALL OBJECTS	WENDT, DAVID W.
10869129	Not Issued	020	06/16/2004	CLASS LOADER	WENDT, DAVID M.
10852887	Not Issued	030	05/25/2004	ENCLOSED OPERATING CHARACTERISTIC SENSOR FOR A BICYCLE COMPONENT INCLUDING AN EMITTER FOR EMITTING AN OPERATING CHARACTERISTIC SIGNAL	WENDT, DAVID L.
10644357	Not Issued	030	08/20/2003	METHOD AND SYSTEM FOR COMPILING JAVA CODE WITH REFERENCED CLASSES IN A WORKSPACE ENVIRONMENT	WENDT, DAVID
10163470	Not Issued	092	06/06/2002	WEB CONTENT MANAGEMENT SOFTWARE UTILIZING A WORKSPACE AWARE JSP SERVLET	WENDT, DAVID
10050272	6842759	150	01/16/2002	SINGLE-INSTANCE CLASS OBJECTS ACROSS MULTIPLE JVM PROCESSES IN A REAL-TIME SYSTEM	WENDT, DAVID
09922539	Not Issued	071	08/03/2001	SYSTEM FOR CONVERTING ELECTRONIC CONTENT TO A	WENDT, DAVID M.

				TRANSMITTABLE SIGNAL AND TRANSMITTING THE RESULTING SIGNAL	
<u>09921020</u>	Not Issued	121	08/02/2001	METHOD AND SYSTEM FOR AUTOMATED RESEARCH USING ELECTRONIC BOOK HIGHLIGHTS AND NOTATIONS	WENDT, DAVID MARK
<u>09900551</u>	Not Issued	041	07/06/2001	METHOD AND SYSTEM FOR AUTOMATED COLLABORATION USING ELECTRONIC BOOK HIGHLIGHTS AND NOTATIONS	WENDT, DAVID MARK
<u>09782780</u>	Not Issued	071	02/13/2001	SCHEDULING OPTIMIZATION HEURISTIC FOR EXECUTION TIME ACCUMULATING REAL- TIME SYSTEMS	WENDT, DAVID M.
<u>09766518</u>	<u>6459392</u>	150	01/19/2001	TECHNIQUE FOR ENCODING A SEQUENCE OF PERIODIC BYTE VALUES WITH VERTICAL CORRELATION	WENDT, DAVID M.
<u>09755241</u>	<u>6820183</u>	150	01/05/2001	METHODS, SYSTEMS, AND COMPUTER PROGRAM PRODUCTS FOR MEMORY POOL MANAGEMENT USING VARIABLE SIZE SUB-POOLS	WENDT, DAVID M.
<u>09735595</u>	Not Issued	161	12/12/2000	METHOD AND APPARATUS FOR SPECULATION BRANCHING IN A JVM/JIT IN SMP ENVIRONMENT	WENDT, DAVID MARK
<u>09735594</u>	Not Issued	168	12/12/2000	METHOD AND APPARATUS FOR DEVELOPER OPTIMIZATIONS FOR JAVA AND JIT COMPILING	WENDT, DAVID MARK
<u>09735592</u>	Not Issued	041	12/12/2000	LANGUAGE EXTENSION FOR LIGHT WEIGHT THREADING IN A JVM	WENDT, DAVID MARK
<u>09714735</u>	<u>6502022</u>	150	11/16/2000	METHOD AND SYSTEM FOR PREVENTING UNSAFE COMMUNICATION DEVICE USAGE IN A VEHICLE	WENDT, DAVID M.
<u>09650849</u>	<u>6842775</u>	150	08/29/2000	METHOD AND SYSTEM FOR MODIFYING MAIL RULES	WENDT, DAVID MARK
<u>09649946</u>	<u>6847989</u>	150	08/29/2000	METHOD AND SYSTEM FOR CREATING MAIL RULES	WENDT, DAVID MARK

				FROM EXISTING MAIL	
<u>09433413</u>	<u>6397948</u>	150	11/03/1999	SHEARING ARRANGEMENT FOR SUBSEA UMBILICALS	WENDT, DAVID E.
<u>08773586</u>	<u>5816042</u>	150	12/27/1996	FLOW DIVERTER SYSTEM FOR MULTIPLE STREAMS FOR GAS TURBINE ENGINES	WENDT, DAVID E.
<u>08472814</u>	<u>5695094</u>	250	06/07/1995	CABINET AND SUPPORTING FRAME FOR LIQUID DISPENSING SYSTEM WITH REMOVABLE RESERVOIR AND HOT TANK	WENDT, DAVID W.
<u>08152625</u>	<u>5431344</u>	150	11/12/1993	SLIDING THROAT GAS TURBINE ENGINE NOZZLE	WENDT, DAVID E.
<u>08139469</u>	<u>5553935</u>	150	10/20/1993	CABINET AND SUPPORTING FRAME FOR LIQUID DISPENSING SYSTEM	WENDT, DAVID W.
<u>08055744</u>	<u>5343134</u>	250	05/03/1993	METHOD FOR CHECKING BRAKE TORQUE	WENDT, DAVID C.
<u>07911526</u>	<u>5213558</u>	150	07/10/1992	EXERCISE DEVICE	WENDT, DAVID W.
<u>07911311</u>	<u>D349316</u>	150	07/10/1992	PHYSICAL EXERCISER	WENDT, DAVID W.
<u>07195252</u>	<u>5052521</u>	150	05/18/1988	STAIRWAY WHEELCHAIR LIFT	WENDT, DAVID W.
<u>07178552</u>	<u>4835843</u>	150	04/07/1988	AUTOMATIC BADGE MAKING MACHINE	WENDT, DAVID W.
<u>07029360</u>	Not Issued	161	03/23/1987	OVERRUNNING CLUTCH FOR OPERATING MECHANISMS FOR PAPER TOWEL DISPENSING CABINETS	WENDT, DAVID W.
<u>06853964</u>	<u>4694812</u>	150	04/21/1986	EXHAUST GAS RECIRCULATION VALVE HAVING INTEGRAL ELECTRONIC CONTROL	WENDT, DAVID L.
<u>06830769</u>	<u>4674335</u>	250	02/19/1986	OIL PRESSURE SENSOR	WENDT, DAVID L.
<u>06752999</u>	<u>4664304</u>	150	07/08/1985	METERING MECHANISM FOR PAPER TOWEL DISPENSER CABINETS	WENDT, DAVID W.
<u>06752998</u>	<u>4662664</u>	150	07/08/1985	LOCK FOR PAPER TOWEL DISPENSER CABINET	WENDT, DAVID W.
<u>06752989</u>	Not Issued	168	07/08/1985	OVERRUNNING CLUTCH FOR OPERATING MECHANISMS FOR PAPER TOWEL DISPENSER CABINETS	WENDT, DAVID W.

<u>06554157</u>	<u>4545357</u>	150	11/22/1983	PROGRAMMABLE TEMPERATURE CONTROL SYSTEM	WENDT, DAVID L.
<u>06292681</u>	Not Issued	161	08/13/1981	ELECTRONIC TUNING DEVICE FOR A MUSICAL INSTRUMENT	WENDT, DAVID L.
<u>06283221</u>	<u>D269699</u>	150	07/14/1981	AIR FRESHENER CONTAINER	WENDT, DAVID W.
<u>06208574</u>	<u>4372490</u>	150	11/20/1980	PULL PAD CONCENTRATED AIR DEODORIZER	WENDT, DAVID W.
<u>06048727</u>	<u>D261776</u>	150	06/15/1979	ELECTRONIC TEACHING UNIT	WENDT, DAVID W.

Inventor Search Completed: No Records to Display.

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81 [Algorithmic modifications to the Jacobi-Davidson parallel eigensolver to dynamically balance](#)

Richard Tran Mills, Andreas Stathopoulos, Evgenia Smirni

June 2001

Proceedings of the 15th international conference on Supercomputing

Full text available: [pdf\(241.76 KB\)](#)

Additional Information: [full citation](#), [abstr:](#)

Clusters of workstations (COWs) and SMPs have become popular and cost effective means of solving shared, dynamic load balancing is central to achieving high performance. Our thesis is that new algorithms with the runtime system. To support this thesis, we illustrate a novel approach for

82 [Scheduling computations on a software-based router](#)

Xiaohu Qie, Andy Bavier, Larry Peterson, Scott Karlin

June 2001

ACM SIGMETRICS Performance Evaluation Review , Proceedings of the modeling of computer systems, Volume 29 Issue 1

Full text available: [pdf\(1.46 MB\)](#)

Additional Information: [full citation](#), [abstr:](#)

Recent efforts to add new services to the Internet have increased the interest in software-based implementing a software-based router, with a particular focus on the main difficulty we encounter the desire to differentiate the level of service for different packet flows, which leads to two funda

83 [Implementing soft real-time agent control](#)

Régis Vincent, Bryan Horling, Victor Lesser, Thomas Wagner

May 2001

Proceedings of the fifth international conference on Autonomous agent

Full text available: [pdf\(124.50 KB\)](#)

Additional Information: [full citation](#), [abstr:](#)

Real-time control has become increasingly important as technologies are moved from the lab into control and autonomy are distributed, due to such issues as precedence constraints, shared resource real-time environment requiring distributed control, and how we modified our existing multi-agent

84 [Power and energy reduction via pipeline balancing](#)

R. Iris Bahar, Srilatha Manne

May 2001

ACM SIGARCH Computer Architecture News , Proceedings of the 28th annual

Full text available: [pdf\(1.06 MB\)](#)

Additional Information: [full citation](#), [abstr:](#)

Minimizing power dissipation is an important design requirement for both portable and non-portable that retains performance while reducing power. The technique, known as Pipeline Balancing (PLB) program by monitoring performance within each program. We analyze metrics for triggering PLB,

85 Data and memory optimization techniques for embedded systems

P. R. Panda, F. Catthoor, N. D. Dutt, K. Danckaert, E. Brockmeyer, C. Kulkarni, A. Vandercappelle, I. April 2001 **ACM Transactions on Design Automation of Electronic Systems (TODAE)**

Full text available:  [pdf\(339.91 KB\)](#)

Additional Information: [full citation](#), [abstr:](#)

We present a survey of the state-of-the-art techniques used in performing data and memory-related indirectly at the memory subsystem, and impact one or more out of three important cost metrics examine architecture-independent optimizations in the form of code transformations. We next c

Keywords: DRAM, SRAM, address generation, allocation, architecture exploration, code transformation, customization, memory power dissipation, register file, size estimation, survey

86 An admission control scheme for predictable server response time for web accesses

Xiangping Chen, Prasant Mohapatra, Huamin Chen

April 2001 **Proceedings of the 10th international conference on World Wide Web**

Full text available:  [pdf\(259.81 KB\)](#)


Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: Internet, PACERS, QoS, admission control, bounded response time, service differentiation

87 Columns: Risks to the public in computers and related systems

Peter G. Neumann

January 2001 **ACM SIGSOFT Software Engineering Notes**, Volume 26 Issue 1




Full text available:  [pdf\(3.24 MB\)](#)

Additional Information: [full citation](#)

88 A static power model for architects

J. Adam Butts, Gurindar S. Sohi

December 2000 **Proceedings of the 33rd annual ACM/IEEE international symposium on Microarchitecture**




Full text available:  [pdf\(136.88 KB\)](#)  [ps\(431.76 KB\)](#)  [Publisher Site](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

89 Compiler controlled value prediction using branch predictor based confidence

Eric Larson, Todd Austin

December 2000 **Proceedings of the 33rd annual ACM/IEEE international symposium on Microarchitecture**

Full text available:  [pdf\(236.58 KB\)](#)  [ps\(850.71 KB\)](#)  [Publisher Site](#)

Additional Information: [full citation](#), [references](#), [index terms](#)

90 Thread-level parallelism and interactive performance of desktop applications

Kristián Flautner, Rich Uhlig, Steve Reinhardt, Trevor Mudge

November 2000 **Proceedings of the ninth international conference on Architectural support for programming languages and operating systems**

Full text available:  [pdf\(234.58 KB\)](#)

Additional Information: [full citation](#), [abstr:](#)

Multiprocessing is already prevalent in servers where multiple clients present an obvious source of

desktop applications. Nevertheless, architects are designing processors that count on the available interactive applications is to respond to user events under human perception bounds rather than

91 Session 8B: embedded systems power management and validation: Power-conscious joint embedded systems

Jiong Luo, Niraj K. Jha

November 2000

Proceedings of the 2000 IEEE/ACM international conference on Compu

Full text available:  pdf(103.54 KB)

Additional Information: [full citation](#), [abstr](#):


In this paper, we present a power-conscious algorithm for jointly scheduling multi-rate periodic task graphs have hard deadlines, the aperiodic tasks can have either hard or soft deadline schedule to accommodate hard aperiodic tasks. Soft aperiodic tasks are scheduled dynamically w

92 Thread-level parallelism and interactive performance of desktop applications

Krisztián Flautner, Rich Uhlig, Steve Reinhardt, Trevor Mudge

November 2000

ACM SIGPLAN Notices, Volume 35 Issue 11

Full text available:  pdf(2.94 MB)


Additional Information: [full citation](#), [abstr](#):

Multiprocessing is already prevalent in servers where multiple clients present an obvious source of desktop applications. Nevertheless, architects are designing processors that count on the available interactive applications is to respond to user events under human perception bounds rather than

93 Process migration

September 2000

ACM Computing Surveys (CSUR), Volume 32 Issue 3

Full text available:  pdf(1.24 MB)

Additional Information: [full citation](#), [abstr](#):

Process migration is the act of transferring a process between two machines. It enables dynamic Despite these goals and ongoing research efforts, migration has not achieved widespread use. W operating systems in particular, process migration is again receiving more attention in both rese


Keywords: distributed operating systems, distributed systems, load distribution, process migrat

94 Improving interactive performance using TIPME

Yasuhiro Endo, Margo Seltzer

June 2000

ACM SIGMETRICS Performance Evaluation Review , Proceedings of the modeling of computer systems, Volume 28 Issue 1

Full text available:  pdf(1.05 MB)

Additional Information: [full citation](#), [abstr](#):

On the vast majority of today's computers, the dominant form of computation is GUI-based user performance. Human-factors research shows that a user's perception of performance is affected l rely on throughput-sensitive benchmarks. While these techniques improve the average performa


Keywords: interactive performance, monitoring

95 A case for user-level dynamic page migration

Dimitrios S. Nikolopoulos, Theodore S. Papatheodorou, Constantine D. Polychronopoulos, Jesús Lab




May 2000



Proceedings of the 14th international conference on Supercomputing

Full text available:  pdf(1.33 MB)

Additional Information: [full citation](#), [abstr](#):

This paper presents user-level dynamic page migration, a runtime technique which transparently memory multiprocessors, with feedback obtained from dynamic monitoring of memory activity. C to the program both at compile time and at runtime in order to improve the accuracy and the tim



- 96 System-level power optimization: techniques and tools
Luca Benini, Giovanni de Micheli
April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAE)**
Full text available:  [pdf\(385.22 KB\)](#) Additional Information: [full citation](#), [abstr](#):
This tutorial surveys design methods for energy-efficient system-level design. We consider electri-
three major constituents of hardware that consume energy, namely computation, communication
also study models for analyzing the energy cost of software, and methods for energy-efficient sol
- 97 Session summaries from the 17th symposium on operating systems principle (SOSP'99)
Jay Lepreau, Eric Eide
April 2000 **ACM SIGOPS Operating Systems Review**, Volume 34 Issue 2
Full text available:  [pdf\(3.15 MB\)](#) Additional Information: [full citation](#), [index terms](#)
- 98 Progress-based regulation of low-importance processes
John R. Douceur, William J. Bolosky
December 1999 **ACM SIGOPS Operating Systems Review , Proceedings of the seventeenth**
Full text available:  [pdf\(1.53 MB\)](#) Additional Information: [full citation](#), [abstr](#):
MS Manners is a mechanism that employs progress-based regulation to prevent resource content
processes. The mechanism assumes that resource contention that degrades the performance of a
MS Manners detects this contention by monitoring the progress of the low-importance process ar

Keywords: process priority, progress-based feedback, symmetric resource contention
- 99 The interactive performance of SLIM: a stateless, thin-client architecture
Brian K. Schmidt, Monica S. Lam, J. Duane Northcutt
December 1999 **ACM SIGOPS Operating Systems Review , Proceedings of the seventeenth**
Full text available:  [pdf\(1.79 MB\)](#) Additional Information: [full citation](#), [abstr](#):
Taking the concept of thin clients to the limit, this paper proposes that desktop machines should
shared pool of computational resources over a dedicated interconnection fabric --- much in the se
devices. The stateless desktop design provides a useful mobility model in which users can transp.
- 100 Borrowed-virtual-time (BVT) scheduling: supporting latency-sensitive threads in a general-p
Kenneth J. Duda, David R. Cheriton
December 1999 **ACM SIGOPS Operating Systems Review , Proceedings of the seventeenth**
Full text available:  [pdf\(1.81 MB\)](#) Additional Information: [full citation](#), [abstr](#):
Systems need to run a larger and more diverse set of applications, from real-time to interactive t
do not address latency requirements or are specialized to complex real-time paradigms, limiting t
Virtual-Time (BVT) Scheduling, showing that it provides low-latency for real-time and interactive

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101 [Procedure placement using temporal-ordering information](#)

Nikolas Gloy, Michael D. Smith

September 1999

ACM Transactions on Programming Languages and Systems (TOPLAS),

Full text available: [pdf\(604.56 KB\)](#)

Additional Information: [full citation](#), [abstr](#)

Instruction cache performance is important to instruction fetch efficiency and overall processor performance and the instruction working set size during execution. This means that the performance of an execution instruction cache conflicts and improves spatial locality. We describe an algorithm for procedure placement

Keywords: code placement, conflict misses, temporal profiling, working-set optimization

102 [Parallel texture caching](#)

Homan Igehy, Matthew Eldridge, Pat Hanrahan

July 1999 **Proceedings of the ACM SIGGRAPH/EUROGRAPHICS workshop on Graphics hardware**

Full text available: [pdf\(1.80 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

103 [Bimodal multicast](#)

Kenneth P. Birman, Mark Hayden, Ozgur Ozkasap, Zhen Xiao, Mihai Budiu, Yaron Minsky

May 1999

ACM Transactions on Computer Systems (TOCS), Volume 17 Issue 2

Full text available: [pdf\(302.06 KB\)](#)

Additional Information: [full citation](#), [abstr](#)

There are many methods for making a multicast protocol "reliable." At one end of the spectrum, delivery, delivery ordering, and perhaps additional properties such as virtually synchronous address in the network, offering "best effort" reliability. Yet none of this prior work has treated stability ..

104 [A software synthesis tool for distributed embedded system design](#)

D.-I. Kang, R. Gerber, L. Golubchik, J. K. Hollingsworth, M. Saksena

May 1999

ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1999 workshop

Full text available: [pdf\(1.64 MB\)](#)

Additional Information: [full citation](#), [abstr](#)

We present a design tool for automated synthesis of embedded systems on distributed COTS-based software layouts, which maps tasks to resources and (2) a constraints solving engine, which allocates Our tool differs from previous work in that it allows (a) use of stochastic (rather than worst-case),

105 MPI support in the Prism programming environment

Steve Sistare, Erica Dorenkamp, Nick Nevin, Eugene Loh

January 1999 **Proceedings of the 1999 ACM/IEEE conference on Supercomputing (CDROM)**

Full text available:  [pdf\(257.56 KB\)](#)


Additional Information: [full citation](#), [references](#), [index terms](#)

Keywords: debugger, message-passing, performance analysis, programming environments, visi

106 Pthreads for dynamic and irregular parallelism

Girija J. Narlikar, Guy E. Blelloch

November 1998 **Proceedings of the 1998 ACM/IEEE conference on Supercomputing (CI**

Full text available:  [html\(82.60 KB\)](#)

Additional Information: [full citation](#), [abstr](#)

High performance applications on shared memory machines have typically been written in a coar programming with a large number of lightweight, parallel threads has several advantages, includ adaptability to a changing number of processors. The programmer can express a new thread to e

Keywords: Pthreads, dynamic scheduling, irregular parallelism, lightweight threads, multithread

107 A task- and data-parallel programming language based on shared objects

Saniya Ben Hassen, Henri E. Bal, Criel J. H. Jacobs

November 1998 **ACM Transactions on Programming Languages and Systems (TOPLAS),**

Full text available:  [pdf\(434.44 KB\)](#)

Additional Information: [full citation](#), [abstr](#)

Many programming languages support either task parallelism, but few languages provide a unifor parallelism. We present a programming language and system that integrates task and data paral replicated. Objects may also be partitioned and distributed on several processors.Task parallelism


Keywords: data parallelism, shared objects, task parallelism

108 Vclusters: a flexible, fine-grained object clustering mechanism

Mark L. Mcauliffe, Michael J. Carey, Marvin H. Solomon

October 1998 **ACM SIGPLAN Notices , Proceedings of the 13th ACM SIGPLAN confere**

Volume 33 Issue 10

Full text available:  [pdf\(2.07 MB\)](#)

Additional Information: [full citation](#), [abstr](#)

We consider the problem of delivering an effective fine-grained clustering tool to implementors a *mechanisms*, as contrasted with earlier work that concentrates on clustering *policies* (deciding w ineffective and/or difficult to use and may lead to poor space utilization on disk and in the disk bl

109 Minimum cost adaptive synchronization: experiments with the ParaSol system

Edward Mascarenhas, Felipe Knop, Reuben Pasquini, Vernon Rego

October 1998 **ACM Transactions on Modeling and Computer Simulation (TOMACS), vo**

Full text available:  [pdf\(265.07 KB\)](#)

Additional Information: [full citation](#), [abstr](#)

We present a novel adaptive synchronization algorithm, called the minimum average cost (MAC) multithreaded system for parallel simulation on shared- and distributed-memory environments, c algorithm is based on minimizing the cost of synchronization delay and rollback at a process, wh

Keywords: ParaSol, adaptive synchronization, optimal delay, optimistic synchronization, parallel



110 Special issue on natural language generation: Collaborative response generation in planning

Jennifer Chu-Carroll, Sandra Carberry

September 1998

Computational Linguistics, Volume 24 Issue 3

Full text available:

 [pdf\(3.45 MB\)](#)  [Publisher Site](#)

Additional Information: [full citation](#), [abstract](#)


In collaborative planning dialogues, the agents have different beliefs about the domain and about the paper, we present a plan-based model for response generation during collaborative planning, based on focus on identifying strategies for content selection when 1) the system initiates *information-sharing*

111 Guidance for the use of the Ada programming language in high integrity systems

B. A. Wichmann

July 1998

ACM SIGAda Ada Letters, Volume XVIII Issue 4

Full text available:  [pdf\(2.93 MB\)](#)

Additional Information: [full citation](#), [abstract](#)

This paper is the current result of a study by the ISO HRG Rapporteur group which is being circulated. Two recent meetings of the group or have made substantial e-mail comments are: Praful V. Critical Systems, UK), Dan Craigen (ORA, Canada), Nick Johnson MoD, UK), Stephen Michell (Car

112 Automatic modeling of file system workloads using two-level arrival processes

Peter P. Ware, Thomas W. Page, Barry L. Nelson

July 1998

ACM Transactions on Modeling and Computer Simulation (TOMACS), Volume 8 Issue 3

Full text available:  [pdf\(220.19 KB\)](#)

Additional Information: [full citation](#), [abstract](#)

This article describes a method for analyzing, modeling, and simulating a two-level arrival-counting process. The process is large, as is the case in our motivating application which requires analyzing and representing network trace data characterizing communication patterns between pairs of computers.

Keywords: clustering, data replication, file access patterns, file system, input modeling, replication

113 Models and languages for parallel computation

David B. Skillicorn, Domenico Talia

June 1998

ACM Computing Surveys (CSUR), Volume 30 Issue 2

Full text available:  [pdf\(298.05 KB\)](#)

Additional Information: [full citation](#), [abstract](#)

We survey parallel programming models and languages using six criteria to assess their suitability: easy to program, should have a software development methodology, should be architecture-independent, provide accurate information about the cost of programs. These criteria reflect our belief that development methods, taxonomy

Keywords: general-purpose parallel computation, logic programming languages, object-oriented development methods, taxonomy

114 "Dynamic-fault-prone BSP": a paradigm for robust computations in changing environments

Spyros C. Kontogiannis, Grammati E. Pantziou, Paul G. Spirakis, Moti Yung

June 1998

Proceedings of the tenth annual ACM symposium on Parallel algorithms and architectures

Full text available:  [pdf\(1.43 MB\)](#)


Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

115 An empirical validation of a contingency model for information requirements determination

Mohammed El Louadi, Dennis F. Galletta, Jeffrey L. Sampler

June 1998

ACM SIGMIS Database, Volume 29 Issue 3

Full text available:  [pdf\(1.75 MB\)](#)

Additional Information: [full citation](#), [abstr](#)

A contingency model for system development was subjected to several conceptual and operation. fit between development project uncertainty and the strategy for determining information require prototypes to discover requirements. This study introduced a new IRD strategy construct that foc

Keywords: contingency model, fit model, information requirements determination, systems dev

116 Navigating in information spaces: Information foraging models of browsers for very large dc

Peter Pirolli, Stuart K. Card

May 1998

Proceedings of the working conference on Advanced visual interfaces

Full text available:  [pdf\(4.29 MB\)](#)

Additional Information: [full citation](#), [abstr](#)

Information Foraging (IF) Theory addresses user strategies and technology for seeking, gathering interfaces: the Scatter/Gather browser for large document collections, and the Butterfly interface model, ACT-IF, models observed users by assuming that they have heuristics that optimize their



Keywords: cognitive models, information foraging theory, information retrieval

117 Pipeline gating: speculation control for energy reduction

Srilatha Manne, Artur Klauser, Dirk Grunwald

April 1998

ACM SIGARCH Computer Architecture News , Proceedings of the 25th an

Full text available:  [pdf\(1.59 MB\)](#)  [Publisher Site](#)

Additional Information: [full citation](#), [reference](#)

118 Concurrency control: methods, performance, and analysis

Alexander Thomasian

March 1998

ACM Computing Surveys (CSUR), Volume 30 Issue 1

Full text available:  [pdf\(427.18 KB\)](#)

Additional Information: [full citation](#), [refer](#)


Keywords: Markov chains, adaptive methods, concurrency control, data contention, deadlocks, restart-oriented locking methods, serializability, thrashing, two-phase locking, two-phase proces

119 Input/output access pattern classification using hidden Markov models

Tara M. Madhyastha, Daniel A. Reed

November 1997

Proceedings of the fifth workshop on I/O in parallel and distributed system

Full text available:  [pdf\(1.46 MB\)](#)

Additional Information: [full citation](#), [references](#), [citing](#), [inde](#)

120 Fast detection of communication patterns in distributed executions

Thomas Kunz, Michiel F. H. Seuren

November 1997

Proceedings of the 1997 conference of the Centre for Advanced Studie

Full text available:  [pdf\(4.21 MB\)](#)

Additional Information: [full citation](#), [abstr](#)

Understanding distributed applications is a tedious and difficult task. Visualizations based on proc the application. The visualization tool we use is Poet, an event tracer developed at the University user with the desired overview of the application. In our experience, such tools display repeated